



**IEEE Student Branch  
Model Engineering College, Thrikkakara  
IEEE's 125<sup>th</sup> Anniversary Celebrations**

**Verilog® HDL and FPGA Prototyping Workshop**

**Application Form**



Name :

Student/Faculty :

Branch and Batch :

Institution :

Are you an IEEE Member (Yes/No) :

If you are an IEEE member then,

Membership type :

Member Number :

Address for correspondence :

Telephone : (Residence) (Mobile)

E-mail :

Signature of Applicant :



Convener

**NB:**

- Selected participants would be intimated by E-Mail.
- Accommodation will NOT be provided.
- Workshop fees covers the lunch and refreshments on the workshop days.
- Please pay the workshop fee by cash on the opening day of workshop. Cheques will not be allowed.
- Please follow the rules and regulations to be followed in the laboratories where hands-on sessions are conducted.
- If you are an IEEE member, bring your IEEE badge at the time of registration and also fill the membership number if you want to avail the discount.
- More details can be found at the following website:

[http://www.mec.ac.in/home/ec/cejo/web/FP\\_2009.htm](http://www.mec.ac.in/home/ec/cejo/web/FP_2009.htm)