

IEEE's 125th Anniversary Celebrations – Technical Workshop Marathon

Course No.	Workshops	Tentative Schedules	Entry Fees	Prerequisites
1	TMS320C6713 DSK and Code Composer Studio	May 18 – May 23, 2009	Rs. 300	Only students who have completed their sixth semester courses, and faculty members can attend hands-on sessions.
2	Introduction to GNU/Linux and Free and Open Source EDA Tools	May 25 – May 29, 2009	Entry Free	No prerequisites for attending
3	Verilog® HDL and FPGA Prototyping	June 1 – June 6, 2009	Rs. 300	Only students who have completed their sixth semester courses and faculty members can attend hands-on sessions.
4	Introduction to ASIC Design Using Alliance VLSI CAD Tools and Physical Design Using Magic	June 8 – June 9, 2009	Entry Free	Completed the courses VLSI Design and ASIC Design
5	GNU Octave and Scilab	June 10 th , 2009	Entry Free	No prerequisites for attending
6	Circuit Simulation Using Ngspice and Qucs	June 11 – June 12	Rs. 100	No prerequisites for attending

Course nos. 1, 3, 6 – IEEE members can waive the course fee at the time of registration if they produce their IEEE member badge.

All the courses can be attended by B. Tech/M. Tech students and faculty members of all departments with the above conditions.