VLSI Technology and Design, 17th February to 21st February of 2009

Date	0930 hrs. - 1055 hrs.		1105 hrs. - 1230 hrs.		1330 hrs. - 1455 hrs.		1505 hrs. - 1630 hrs.
17.02.09	Inauguration and Keynote Address **"Present and Future Perspectives of Integrated Circuits – The Challenges"** Dr.J.P Raina,VIT, Vellore		**"Introduction to VLSI Design and Design Flow"** VLSI Design Group, Wipro Technologies, Kochi		**"Design for Testability"** VLSI Design Group, Wipro Technologies, Kochi		**"Design for Testability"** VLSI Design Group, Wipro Technologies, Kochi
18.02.09	**"Hardware Software Co-Design"** Dr.J.P.Raina,VIT, VelloreDr. Lisa Mathew, Sri Venkateswara College of Engineering	**TEA BREAK**	**"Algorithms For VLSI Design Automation"** Dr. Lisa Mathew, Sri Venkateswara College of Engineering	**LUNCH BREAK**	**"Timing Issues in VLSI Design"** VLSI Design Group, Wipro Technologies, Kochi	**TEA BREAK**	**"Algorithms For VLSI Design Automation"** Dr. Lisa Mathew, Sri Venkateswara College of Engineering
19.02.09	**ULSI - Prospects and Challenges** Prof. Nanditha Dasguptha, IIT Madras		**"ULSI - Prospects and Challenges"** Prof. Nanditha Dasguptha, IIT Madras		**"MEMS Processing"** Prof. Enakshi Bhattachariya, IIT Madras		**"BioMEMS"** Prof. Enakshi Bhattachariya
20.02.09	**Integrated Optics and Highly Coherent Light** Prof. Bijoi Krishna Das, IIT Madras		**"Silicon Photonics"** Prof. Bijoi Krishna Das, IIT Madras		**"Compact Modelling"** Prof. Anjan Chakravorthy		**"Compact Modelling"** Prof. Anjan Chakravorthy
21.02.09	**Open Source EDA Tools for VLSI Design** Cejo K. L. Model Engineering College		**"Device Modeling: The art of making Approximations"** Prof. S. Karmalkar, IIT Madras		**"Thinking and communication skills for Research"** Prof. S. Karmalkar (up to 1515 hrs.)		**Valedictory Function** (1530 hrs. to 1630 hrs.)